IN THE UNITED STATES DISTRICT COURT FOR THE EASTERN DISTRICT OF TEXAS MARSHALL DIVISION

OPTi, INC.	§	
	8	
	§	Case No. 2:07cv21
VS.	§	
	§	
APPLE, INC., ET AL	§	

OPTi, Inc.'s FINAL Exhibit List

PTX 6

PTX 5 PTX 4

17/10/11/14

PTX 3 PTX 2 PTX 1

4/11/09

PTX 9

11/1/09

APPLE 10K

Spreadsheet re cost

not the chipset value) 403 (figures are speculative and address EMV numbers)

403 (forecasts are speculative and address EMV numbers,

403 (forecasts are speculative and address EMV numbers,

appropriate measure of damages)

(chipset value) and instead address the EMV, which is not 403 (figures do not address the appropriate royalty base appropriate measure of damages)

403 (figures do not address the appropriate royalty base

(chipset value) and instead address the EMV, which is not

not the chipset value)

without supporting testimony)

403 (voluminous SEC filing with limited probative value

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PTX 8

PTX 7

4/17/09 11/109

Product Forecast Detail (Q3 FY03 and Q4 FY03)

Spreadsheet re Product Forecast Detai

Spreadsheet re Management Line of Business Reporting

Spreadsheet re Management Line of Business Reporting

Spreadsheet re Q2'05 Asics PPV Analysis

Spreadsheet re USA Data

4/17/09

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ADMITTED DESCRIPTION OF EXHIBITS

OBJECTIONS

WITHDRAWN

OFFERED

OPTi, Inc

Apple, Inc

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OPTi, Inc. v. Apple, Inc

EXHIBIT AND WITNESS LIST-CONTINUATION

Case 2:07-cy-0021-CE

UNITED STATES DISTRICT COURT EASTERN DISTRICT OF TEXAS

OPTI'S EXHIBIT AND WITNESS LIST

APPLE'S OBJECTIONS THERETO

17-Apr-2009	Charles Everingham		Presiding Indoe
Court Reporter	Samuel Franklin Baxter	Plaintiff's Attorney	
Courtroom Deputy		Defendant's Attorney	Case Number: 2:07-cv-00021-CE

17-Apr-2009	Charles Everingham	westung stude
Court Reporter	Samuel Franklin Baxter	Plaintiffs Attorney
Courtroom Deputy	Болиция стиливу	Defendants Attance

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en Apple and 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license agreement may confuse jury)	MPEG-4 Audio Patent License Agreement between Apple and Via				8
regarding the license, admission of entire license agreement may confuse iury)					DTV 25
403 (limited probative value beyond any expert testimony	7				P1X 34
	PCI Patent License Agreement between PCI and Apple	***************************************			F1 X 33
	akin Lans re	7	20	17/09	200
regarding the license, admission of entire license agreement may confuse jury)					PTX 32
403 (limited probative value beyond any expert testimony	en Patent Incentives and Apple				71 X 31
801	Apple Press Release re Apple to Use Intel Microprocessor Beginning in 2006	15	1,0	130/11/14	N 30
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	Letter re OPTi Patents Related to Chipset Technology	4	06	20 11/2	P X 20
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nearsay (801).	WITHDRAWN	1			PTX 25
403 (fails to address any testing of claimed invention),	Memo re 82C557 Debug Status and Activity Report	7	109	11/4	F 1 × 24
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403 (miscellaneous compilation of documents); Hearsay (801).	viper Applications Training Manual	7	109	4/17/09	PTX 23
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offered to prove dates and status of Viper development at various times).		7	09	4/17/09	DTV 21
403 (miscellaneous compilation of a small subset of	viper Diagrams				
	US Patent 5,768,624	7	Del	11.11.	PTX 20
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71X 36				and the second s	MPEG-2 Patent Portfolio License between MPEG LA and Apple	403 (limited probative value beyond any expert testimony regarding the license, admission of entire license.
PTX 37				وم مراد المراد ا	AAC Patent License Agreement between AT&T, Doiby, Fraunhofer-Gesellschaft, Sony and Apple	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 38				distribution of the second	Digital Audio System License Agreement between Dolby and Apple	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 39		4/11/09			Serial Bus Semiconductor License Agreement between Apple and Matsushita	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 40					IEEE 1394 Open Host Controller Interface Patent License Agreement	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 41					Software License Agreement between Audio MPEG, Societa Italiana Per Lo Sviluppo Dell'Elettronica and Apple	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license.
PTX 42				, voc.	Settlement and License Agreement between Apple, IP Innovation and Technology Licensing	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
71X 43					Settlement and License Agreement between Apple and Premier International	403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
71X 44					Settlement and License Agreement between Apple and Bust.com	403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 45					MPEG-4 Systems Patent Portfolio License between MPEG and Apple	agreement may confuse jury), 408, 801. 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 46					License Agreement between Apple and Global Patent Holdings	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PIX 4/					License Agreement between Apple and MLR	agreement may confuse jury) 403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
PTX 49						403 (limited probative value beyond any expert testimony regarding the license, admission of entire license
12.19					License Agreement between Apple and Hewlett-Packard	403 (limited probative value beyond any expert testimony regarding the license, admission of entire license agreement may confuse linux)

	CMC-3 Design Specification	7		101111		F) A / 8
	Request for Designs Approval					200
	ASIC Decimentation Bouting Boundary			77110		PTX 77
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	ASIC Documentation Pourties Region 4					PTX 73
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rearsay (801), 403, 1002	(Comparative Income Statement 1993 - 2008)	7		17/109		77 60
Line (804) 400 4000	Exhibit 4 to Expert Report of Boy Weinstein in a ODT AMD			-		PTX 61
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	WITHDRAWN			•		PTX 56
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403 (limited probative value beyond any expert testimony	Limited IEEE Serial Bus License IEEE 1394-1995 High					- T
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agreement may confuse jury)	Serial Bus Comisonal International			•		PTX 53
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403 (limited probative value beyond any expert testimony	Lechnology Licensing Agreement between Apple and Sun	· · ·		1 1 1 1		
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403 (limited probative value beyond any expert testimony	License Agreement between Apple and Biax					T 1 X 5 T
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OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITTED	MARKED	OFFERED	NO.	1,00
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1	NO.	OFFERED	THANKIN	TALLING.	DESCRIPTION OF EXHIBITS	OBJECTIONS
PTX 79					Calendar of Mike Collins	
71X80					Letter acknowledging the prototype acceptance for the ASIC	
PTX 81		11/14			List of PCI concerns for the Metro project	
PTX 82					List of PCI concerns for the Metro project	-
PTX 83		4/17/09			Mobile Triton System Controller (MTSC) and Data Path (MTDP) Component Spec. (450DOC006842 -	
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PTX 85		11.1/4			Triton VE External Design Specification (450DOC009068 -	
PTX 86		10101			Patent License Agreement between Dambus Inc. and	1001100
70 VTC		4/17/09		<	Advanced Micro Devices, Inc., December 30, 2005	402/403 (irrelevant AMD license)
7 × 0/		0, 1, 4			nologies,	402/403 (irrelevant AMD license)
PTX 88		1011		7		
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> 9	`	4/17/09	~~··	7	Pentium 4 800 MHz Chipset License Agreement between ATI 402/403 (irrelevant AMD license) Technologies, Inc. and Intel Corporation, June 23, 2003	402/403 (irrelevant AMD license)
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PTX 93		4/11/09		-	Pangea Unified North Bridge and I/O Controller V.1.0 (part 1)	403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
PTX 94		4/17/09		1	Pangea Unified North Bridge and I/O Controller V.1.0 (part 2)	Jury). 403 (technical document without any supporting testimony has limited probative value and will tend to confuse the limit.
PIX 95		4/17/09		/.	Pangea Unified North Bridge and I/O Controller V.1.0 (part 3)	403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
PTX 96			:		Elastic Interface Micro Architecture Specification v.55	403 (technical document without any supporting testimony has limited probative value and will tand to confuce the
						jury).

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		ED	ADMITTED	DESCRIPTION OF EXHIBITS	OBJECTIONS
71/9/				Apple Processor Interconnect (API) Specification, rev. 1.01	403 (technical document without any supporting testimony
PTX 98			7	Pannea I history North Distriction	has limited probative value and will tend to confuse the jury).
חדע חח	7/17/09	9	7	norm Bridge and I/O Controller V.1.02 (part 1)	403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
7 7 99	4/17/09	00		Pangea Unified North Bridge and I/O Controller V.1.02 (part 2)	403 (technical document without any supporting testimony has limited probating.
PTX 100				Pannea Unified North Bridge and 100	jury).
DTV 404	4/17/09	20	1	on troiler V.1.02 (part 3)	r V.1.02 (part 3) 403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
T	14/17/16	76	`	Intrepid2 block diagram	jury). 403 (technical document without any supporting testimony)
PTX 102			7		ilds liftlied probative value and will tend to confuse the jury).
DTV 103	4/17/09	109	1		403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
	4/17/09	09		Intrepid block diagram	403 (technical document without any supporting testimony has limited probative value and will took to probative value.
PTX 104				K2 Block Diagram	ury).
DTV 108	Pa/17/4	09	-		403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
7	4/17/20	2		Shasta ERS External Reference Specifications	Jury). 103 (technical document without any supporting testimony
PTX 106	100				jury).
77 407	4/17/09	9	-	רושאים ביוטרע ביומאומווו [4]	403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
70/	11		•	U2 ERS, Rev. 0.7	lury).
PTX 108	17/17/09	99	-		has limited probative value and will tend to confuse the
	4	5		UniNorth ERS, Rev. 0.9	403 (technical document without any supporting testimons)
PTX 109	117109	99			has limited probative value and will tend to confuse the jury).
	4/17/09	20		h	403 (technical document without any supporting testimony has limited probative value and will tend to confuse the
PIX 110			A	Form 10-K for the fiscal year ended	Jury).
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	1	ED .		COLL LINGS	DESCRIPTION OF EXHIBITS	OBJECTIONS
P1X 111		_			Apple Computer, Inc. Form 10-K for the fiscal year ended September 30, 2000	403 (voluminous SEC filing with limited probative value
1 / 1 / 1 / 2				المعادية الم	Apple Computer, Inc. Form 10-Q for the period ended March 31, 2001	403 (voluminous SEC filing with limited probative value without supporting testimony)
P1X 113				T	Apple Computer, Inc. Form 10-Q for the period ended June 30, 2001	403 (voluminous SEC filing with limited probative value without supporting texting and
PTX 114		Palrily			Apple Computer, Inc., Form 10-K405 for the fiscal year ended September 29, 2001	403 (voluminous SEC filing with limited probative value without supporting testimony)
PTX 115				والمناسبة	Apple Computer, Inc. Form 10-Q for the period ended June 29, 2002	403 (voluminous SEC filing with limited probative value without supporting testimony)
PTX 116		4/17/09		7	Apple Computer, Inc. Form 10-K for the fiscal year ended September 28, 2002	403 (voluminous SEC filing with limited probative value without supporting testings)
PTX 117				1	WITHDRAWN for duplicity. See PTX 116	manage supporting teamingry).
FIX 118					Apple Computer Company Inc. Form 10-Q for the period ended December 28, 2002	403 (voluminous SEC filing with limited probative value without supporting testimony)
PIX 119					Apple Computer Company Inc. Form 10-Q for the period ended June 28, 2003	403 (voluminous SEC filing with limited probative value
021 X 120					Apple Computer Company Inc. Form 10-Q for the period ended December 27, 2003	403 (voluminous SEC filing with limited probative value
PIX 121					Apple Computer Company Inc. Form 10-Q for the period ended March 27, 2004	403 (voluminous SEC filing with limited probative value
P1X 122				No. of Street,	Apple Computer Company Inc. Form 10-Q for the period ended June 25, 2005	403 (voluminous SEC filing with limited probative value
P1X 123		4/17/09		K	Apple Computer, Inc. Form 10-K for the fiscal year ended September 24, 2005	
PTX 125						403 (voluminous SEC filing with limited probative value without supporting testimony).
PTX 126				17,000	ت. ا	403 (voluminous SEC filing with limited probative value without supporting testimony).
PTX 127				Marie Carlos		
PTX 128					period	403 (voluminous SEC filing with limited probative value without supporting testimony).
PTY 120					period	
DTV 420						403 (voluminous SEC filing with limited probative value without supporting testimony).
PTX 131		717/09		K	period	403 (voluminous SEC filing with limited probative value without supporting testimony).
	-				ended June 28, 2008	403 (voluminous SEC filing with limited probative value without supporting testimony).

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070	NO. OFFERED			PESCAL HON OF EXHIBITS	OBJECTIONS
P1X 132	4/17/09		/	Apple Inc. Form 10-K for the fiscal year ended September 27,	403 (voluminous SEC filing with limited probative value
PTX 133			the complete of the complete o	Apple Unveils New Generation G4 iBooks Starting at Just	without supporting testimony).
PTX 134			المراجعة المسادية	"Apple Introduces Xserve G5", January 6, 2007	
_				Apple Announces a Faster More Affordable eMac April 13 2004	
PTX 136				Apple Introduces New PowerBook G4 Line, April 19, 2004	104
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				Apple Unveils New MacBook Featuring Intel Core Duo	
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		-		Apple Introduces Xserve with Quad 64-bit Xeon Processors, August 7, 2006	·
7 > 4				Apple Unveils New Mac Pro Featuring Quad 64-bit Xeon	
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PIX 149	<u> </u>		-	U3 ERS, Rev. 0.5	Hearsay (801), 403
	177/09		_		has limited probative value and will tend to confuse the
PTX 150	100/1-1/4			. Form 10-K for the fiscal year ended	Jury). 403 (voluminous SEC filing with limited probative value)
PTX 151				OPTi, Inc. Form 10-K for the fiscal year ended March 31, 2006 801; 403 (voluminous SEC filing with limited probation).	without supporting testimony). 801: 403 (voluminous SEC filing with limited probeting)
PTX 152			0	OPTI, Inc. Form 10-K for the fiscal year ended March 34	value without supporting testimony).
PTX 153			2		Value without supporting testimony)
Var VTG	71.7109		3 (OP II, Inc. form 10-Q for the fiscal quarter ended September 30, 2008	801; 403 (voluminous SEC filing with limited probative
- - - -	(1)			Kodiak ERS PRELIMINARY Rev. 0.2	403 (technical document without any supporting testimony).
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PTX 156	7/1/1/02		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	006	FRE 106
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PTX 158	4)7 00	-	7	04	FRE 106
				cinac buyer Frome Survey, March 2004	FRE 106

	Intel Invention Disclosure			14/17/09	FIX 1/4
supporting testimony has limited probative value and will tend to confuse the iury).		7	-72	4117109	
Hearsay (801), 403 (technical document without any	Power Mac G5 Developer Note	•		** 	FIX 1/3
403 (technical document without any supporting testimony has limited probative value and will tend to confuse the jury).	ŏ	1		11/109	
Jury).					PTX 172
403 (technical document without any supporting testimony has limited probative value and will tend to confuse the	Power Mac G5 Developer Note, April 29, 2005	\	<u> </u>	11/11/09	
403 (technical document without any supporting testimony has limited probative value and will tend to confuse the jury).	Processor,	7		4/17/09	DTY 171
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403 (technical document without any supporting testimony has limited probative value and will tend to confuse the	PowerPC G5 Performance Primer, Technical Note TN2087				
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agreement may confuse jury).					DTY 166
403 (limited probative value beyond any expert testimony regarding the license, admission of entire license	PEG, Inc. a, S.P.A.				F1X 103
403 (limited probative value beyond any expert testimony regarding the license, admission of entire license agreement may confuse jury).	ן שיטי	<	2	4/17/09	P1X 164
403 (outgoing license, not relevant, analogous or probative)		7	7	4/11/09	
agreement may confuse jury).	_1				PTX 163
403 (limited probative value beyond any expert testimony regarding the license, admission of entire license	nent 2 to Patent License Agreement between n Licensing S.A. and Apple Computer, Inc., October			-	25
agreement may confuse jury).	000	(re-man-de-)			DTY 160
403 (limited probative value beyond any expert testimony	Amendment 1 to Patent License Agreement between Thomson Licensing S.A. and Apple Computer Inc. October				712101
agreement may confuse jury).		-			DTV 461
403 (limited probative value beyond any expert testimony regarding the license, admission of entire license.	Faterit License Agreement between Thomson Consumer 403 (limited probative value beyond any expert te Electronics GmbH and Apple Computer, inc., January 1, 2001 regarding the license admission of entire license.				3
FRE 106	Power Mac G5: Buyer profile Study, March 2004	7		417100	PTX 159
OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITTED	MARKED	OFFE	NO. NO.
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Hearsay (801), 403.	Firestar v3.2 Errata to be fixed in FS200 and UltraFire	محصيمسم			6	PTX 196
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agreement may confuse jury).					4	PTX 104
regarding the license, admission of entire license						
403 (limited probative value beyond any expert testimony	Opti Inc. Technology License Agreement				ω	PTX 193
editori wili editidse tile July).	PCI System Architecture (Second Edition)		0	4/17/00	2	PTX 192
403 (only second edition is asserted as prior art; third	PCI System Architecture (Third Edition)	K		17/17/09		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
801; 403 (voluminous SEC filing with limited probative value without supporting testimony).	2000					7 7 7
Hearsay (801), 403.		The report of the state of the				YTG
March 31, 2005 801; 403 (voluminous SEC filing with limited probative value without supporting testimony).	OP II, Inc. Form 10-K for the fiscal year ended March 31, 200	· I de la company de la compan			P C	1 / 1 / 1 O
801; 403 (voluminous SEC filing with limited probative value without supporting testimony).						P1X 187
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801; 403 (voluminous SEC filing with limited probative value without supporting testimony).	OPTi, Inc. Form 10-K for the fiscal year ended December 31, 2001	-			5 5	P1X 185
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801; 403 (voluminous SEC filing with limited probative value without supporting testimony).	OPTi, Inc. Form 10-K for the fiscal year ended December 31, 1995	الم			2	PTX 182
801; 403 (voluminous SEC filing with limited probative value without supporting testimony).	Form 10-K: Annual report pursuant to section 13 or 15 (d) of the securities exchange act of 1934					P1X 181
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801; 403 (voluminous SEC filing with limited probative value without supporting testimony); 408 (re: NVIDIA litigation).	OP II, inc. 2007 Annual Report	Parameters, o			· · ·	7 × 1/9
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801; 403 (voluminous SEC filing with limited probative value without supporting testimony): 408 (re: NVIDIA	OPTi, Inc. 2006 Annual Report				α	
Hearsay (801), 403	ViperMax Scaleable Multimedia PC Chipset	Table Marketing			7	PTX 177
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	CDC Cache & DRAM Controller Target Specification, rev. 1.0	1		4/17/09	બ	PTX 175
OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITTED	MARKED	DATE OFFERED	O. DEF	PLF NO.
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WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset Schematic design of a portion of Viper chipset Schematic design of a portion of Viper chipset PCI local bus specification, Production Version, Revision 2 PCI local bus specification of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82C505 chipset Product Description VT82C505 chipset Product Description VT82C505 in Suit No. 1546 of 1999 in Singapore OPTI Viper Chipset Marketing Document showing EDO support Patent 5,511,226 re System for Generating Snoop Addresses WITHDRAWN Excerpt from Intel Pentium Processor User's Manual of 1993 United States Patent 5,113,514, dated May 12, 1992 Product Description of the Viper Chipset Marketing Disclosure Bulletin dated October 1993 entitled, Micro Channel data streaming input out of the Viper Chipset Marketing Disclosure Bulletin dated October 1993 entitled. Micro Channel data streaming input		United States Patent 5.796 977	7	4/11/09	4	PTX 217
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82C505 chipset Product Description VT82C505 in Sult No. 1546 of 1999 in Singapore OPTI Viper Chipset Marketing Document showing EDO support Patent 5,511,226 re System for Generating Snoop Addresses WITHDRAWN Excerpt from Intel Pentlium Processor User's Manual of 1993 United States Patent 5,113,514, dated May 12, 1992		Publication from IBM Technical Disclosure Bulletin dated October 1993 entitled, Micro Channel data streaming input				
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WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2 PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82c505 chipset Product Description VT82c505 in Suit No. 1546 of 1999 in OPTI Viper Chipset Marketing Document showing EDO support Patent 5,511,226 re System for Generating Snoop Addresses WITHDRAWN		Excerpt from Intel Pentium Processor User's Manual of 199				- 1× < 14
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset Schematic design of a portion of Viper chipset PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82c505 chipset Product Description VT82C505 in Suit No. 1546 of 1999 in OPTI Viper Chipset Marketing Document showing EDO support Patent 5,511,226 re System for Generating Snoop Addresses		WITHDRAWN				P1X 213
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset Schematic design of a portion of Viper chipset PCI local bus specification, Production Version, Revision 2 PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82C505 chipset Product Description VT82C505 in Suit No. 1546 of 1999 in Singapore OPTI Viper Chipset Marketing Document showing EDO support		Patent 5,511,226 re System for Generating Snoop Addresse	7	4/17/09	4	-1275
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2 PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82C505 chipset Product Description VT82C505 in Suit No. 1546 of 1999 in Singapore		OPTI Viper Chipset Marketing Document showing EDO support				7 2 2
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2 PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a System Architecture, State Transition and Block Diagrams as they relate to the VIA VT82c505 chipset		escription v182C5U5 in Suit No. 1546				PTX 211
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2 Pconsideration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7 Prior Art Based on the VIA505 re waveforms captured from a G486VPC motherboard		they relate to the VIA VT82c505 chipset	,			PTX 210
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2 Declaration of Subir Ghosh in Support of Request for Further Consideration of IDS Documents D2-1 through D2-7		Prior Art Based on the VIA505 re waveforms captured from G486VPC motherboard	- Separate S			PTX 209
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics PCI local bus specification, Production Version, Revision 2		Declaration of Subir Ghosh in Support of Request for Furthe Consideration of IDS Documents D2-1 through D2-7	7	4/17/09	.**	PTX 208
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset 557 top level schematics		PCI local bus specification, Production Version, Revision 2	7	100/11/4		PTX 200
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset	403 (miscellaneous compilation o uncorroborated preliminary diagrainventor at OPTI); 801 (to the exteand status of Viper development);	oor top level schematics				TV and
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN Schematic design of a portion of Viper chipset	inventor at OPTI); 801 (to the extra and status of Viper development		7	4/17/09		PTX 205
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN WITHDRAWN	403 (miscellaneous compilation c uncorroborated preliminary diagrams)	schematic design of a portion of Viper chipset		-		
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN WITHDRAWN		WITHDRAWN	الانتقاد مدمان والمدرج			PTX 204
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002 WITHDRAWN		WITHURAWN	47			PTX 203
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998 United States Patent 6,405,291 B1, dated June 11, 2002						CU6 X1d
WITHDRAWN Resume of Subir Ghosh submitted to OPTi United States Patent 5,813,036, dated September 22, 1998			CONTRACTOR			PTX 200
WITHDRAWN Resume of Subir Ghosh submitted to OPTI	403 (patent not asserted in this of	United States Patent 5,813,036, dated September 22, 1998	7	4/17/09		PIX 199
WITHDRAWN	Hogresy (804) And	Resume of Subir Ghosh submitted to OPTi				PIX 198
		WITHDRAWN	,°			PTX 197
ADMITTED DESCRIPTION OF EXHIBITS	OBJECTIONS	DESCRIPTION OF EXHIBITS		OFFERED	NO.	

Hearsay (801), 403	Minutes of the Meeting of the Board of Directors of OPTI, Inc.					PTX 266
	Minutes of the Meeting of the Board of Directors of OPTi Inc.				<u> </u>	PTX 265
Hearsay (801), 403	Letter from Derwin & Siegel to VIA Technologies re OPTi Patents with attached letter dated 8/11/00 to Johnny Lee re same					PTX 264
	Minutes of the Meeting of the Board of Directors of OPTi Inc.	h _{amme} rodiniminah (ministry)				PTX 263
Hearsay (801), 403	Letter from Derwin & Siegel to Carr & Ferrell re Opti-Via enclosing a Notice of Allowance and Issue Fee Due for an Opti patent application dated 4/2/02	and the same of th				PIX 262
						P1X 261
Hearsay (801), 403	Letter from Derwin & Siegel to Carr & Ferrell re Opti-Via and technical issues relating to Via's infringement	Se /a minasymmy				PTX 260
Hearsay (801), 403	Email from Derwin to Carr & Ferrel re VIA / OPTi Question re VIA 530my chipset					PTX 259
Hearsay (801), 403	Letter from Derwin & Siegel to Carr & Ferrell re OPTi - Via re location of board	manus de l'amondante de la constante de la con				PTX 258
Hearsay (801), 403	Email from Derwin to Carr & Ferrell re Latest VIA Timing Diagram	**************************************				PTX 257
Hearsay (801), 403, 901, F	Transcription of Voicemail Message from Doug Derwin to John Ferrell re Via / OPTi					PTX 256
	Federal Express Receipt and Letter from Derwin & Siegel to Carr & Ferrell re OPTi - Via and enclosing system board	, m				PTX 255
Hearsay (801), 403	Fax and Letter from Derwin & Siegel to Carr & Ferrell re OPTi . Hearsay (801), 403 Via and waiver issue					PTX 254
	Letter from Carr & Ferrell to Derwin & Siegel re OPTi - Via and waiver issue					PTX 253
Hearsay (801), 403		17				PTX 252
Hearsay (801), 403	Letter from Derwin & Siegel to Carr & Farrell re OPTi - VIA and extension of deadline					PTX 251
Hearsay (801), 403	Fax and letter re Via's possible purchase of OPTi's patents					PTX 250
	Letter from Wilson Sonsini Goodrich & Rosati re Via Technologies, Inc. and the 906 Patent					P1X 249
	WITHDRAWN					PTX 248
	WITHDRAWN					PTX 247
OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITTED	MARKED	OFFERED	NO.	ELE NO.

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•		DATE OFFERED	MARKED	ADMITTED	DESCRIPTION OF EXHIBITS	OBJECTIONS
PTX 267				,	Minutes of the Meeting of the Board of Directors of OPTI, Inc.	Hearsay (801), 403
PTX 268					Minutes of the Meeting of the Board of Directors of OPTI, Inc.	Hearsay (801), 403
PTX 269					Minutes of the Meeting of the Board of Directors of OPTI, Inc.	
PTX 270				Description (A)	Minutes of the Meeting of the Board of Directors of OPTi, Inc.	Hearsay (801), 403
PTX 271					Minutes of the Meeting of the Board of Directors of OPTi, Inc.	Hearsay (801), 403
PTX 272					WITHDRAWN	
PTX 273	_				WITHDRAWN	
P1X 274				-	OPTi Technology License Agreement re integrated circuits that function as USB Controllers and Core Logic Devices	403
PTX 275				***************************************	Transcription of Voicemail Message from Doug Derwin to John Ferrell re Via / OPTi	Hearsay (801), 403
71X 2/6					Letter to OPTi's counsel Derwin & Siegel re OPTi / Via and belief that no conflict waiver for Carr & Ferrell is needed	Hearsay (801), 403
PTX 277				Lymphageness	Transcription of Voicemail Messages dated 1/13/01, 11/20, 11/9 and 11/2/2000 from Doug Derwin to John Ferrell re VIA / OPTI re simulators	Hearsay (801), 403, 901, F
P1X 278				American (physikamum sapsisgus	Email chain from Derwin & Siegel to Carr & Ferrell re Latest VIA Timing Diagram	
PTX 279				The state of the s	errell to Derwin & Siegel re V	fia Simulation Hearsay (801), 403
PTX 280				and the state of t	Email chain between John Ferrell and Doug Derwin re Timing 403, 408, Hearsay (801)	403, 408, Hearsay (801)
P1X 281					۱۳	Hearsay (801), 403
P1X 282					& Ferrell and Derwin & Siegel re VIA / 530mv chipset	Hearsay (801), 403
F1X 283					l and Carr & Ferrell re nipset re relevant board	Hearsay (801), 403
PTX 284					<u> </u>	Hearsay (801), 403
PTX 285					Carr & Ferrell to Derwin & Siegel re VIA / OPTI interest in proceeding with licensing discussions	Hearsay (801), 403

PI R NO	THE	Tr.	משעום איוו		NEW CONTRACTOR OF THE PROPERTY	
i i i	NO.	OFFERED	MAKKED	ADMILLED	DESCRIPTION OF EXHIBITS	OBJECTIONS
P1X 286					Email from Carr & Ferrell to Derwin & Slebert re Opti v. VIA and 505 datasheets	
PTX 287					Transcription of Voicemail Message from Doug Derwin to John Ferrell re VIA / OPTi Arbitration Offer	402, 403, 408, Hearsay (801), 901, F
PTX 288					Letter from Derwin & Siegel to Carr & Ferrell re OPTi - Via re Notice of Allowance and Issue Fee Due for OPTi patent application	Hearsay (801), 403
PTX 289				,	tter from Derwin & Siegel to Carr & ing Notice of Allowance and Issue Feication stamped April 2, 2002	Ferrell re OPTi -Hearsay (801), 403 e Due for OPTi
PTX 290			·		stating Carr & Ferrell contact Paul Shan of	Hearsay (801), 403
PTX 291				,	Letter from Derwin & Siegel to Carr & Ferrell re OPTi - Via enclosing copy of Patent No. 6,405,291 B1 issued to OPTi on June 11, 2002	Hearsay (801), 403
PTX 292				,	Subpoena in a Civil Case for Derwin & Siegel, LLP	Hearsay (801), 403
P1X 293				- Charles and the same of the	Letter from Derwin & Siegel to VIA Technologies re patent claims including VIA Northbridge products	Hearsay (801), 403
7 IX 294					Printout of court docket for U.S. District Court Northern District of California Intel v. Via	Hearsay (801), 403
F1X 295					Mutual Nondisclosure Agreement by and between OPTi, Inc. and VIA Technologies	Hearsay (801), 403
7 X 296					Architecture and Block Diagram for the VIA Technologies 82C505 System: Figures 1 through 7 (pages 9 - 15 from Ghosh EX 20)	
PTX 297					Timing Diagrams	Hearsay (801), 403
P1X 298					Email from Siegel to Ferrell re Latest VIA Timing Diagram attaching VIA Timing Diagram Showing 505 NO Pre Snoop with Internal Registers Set	Hearsay (801), 403
PTX 299				فللت مستول و المراجعة و	Emails between various parties re Via Simulation	
P1X 300					am/subject to	402, 403, 408, Hearsay (801)
F1X 301					Emails between various parties re Latest VIA Timing Diagram	Iming Diagram Hearsay (801), 403
PTX 303						Hearsay (801), 403
7						Hearsay (801), 403, 408
T 1 \ 304				Control of the Contro	Fax from Ferrell to Derwin forwarding email re Prior Art Based on the VIA505	
P1X 305					Letter from Change to Derwin re OPTi patents	

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	NO. OFFE	ŒD	MARKED	ADMITTED	DESCRIPTION OF EXHIBITS	OBJECTIONS
PTX 306					Transcription of Voicemail Message from Derwin to Ferrell re	Hearsay (801), 403, 408
PTX 307					Letter from Derwin & Siegal to NV/dia re ODTi patents	
PTX 308				The state of the s	Asic Release Check List	Hearsay (801), 403, 408
PTX 309				-	ASIC Engineering Change Motice	
PTX 310				application of the second seco	ASIC Release Checklist	
PTX 311					ASIC Engineering Change Notice	
PTX 312					160QFP DMC Design Specification	
PTX 313				التراجية المتحددة	Biner - 2 folder	
PTX 314				- ACAMAN AND AND AND AND AND AND AND AND AND A	DMC-2 Multi-Source Design Specification	
PTX 315					Gate array blue notebook outline	
PTX 316				paradean Table State Sta	ASIC documentation routing request for designs approval	
P1X 317					CMC-2 Design Specification	
PIX 318					Letter from Compaq to Texas Instruments re Prototype	
PTX 319					Acceptance for the Market	The state of the s
PTX 320				(CMC-2 Phys Pin file	
PTX 321					Functional Test Vectors	
P1X 322				- Company of the Comp	List of PCI concerns for the Metro	
P1 X 323				1	Archive Box 70 Folder, ASIC Release Checklist	
T1A 324					CMC-3 Design Specification	
DTX 326				,	Mike Collins Calendar	
17.020	1/4	120/14	·	<u> </u>	Presentation to provide in	sight into PCI- Hearsay (801), 403
PTX 327				ا	WITHDRAWN for duplicity. See PTX 326	
P1X 328					PCI-X protocol addendum to the PCI local bus specification,	
PTX 329					WITHDBAWA	
PTX 330					WITHDRAWN	
PTX 331					U.S. Patent No. 5.710.906	
PTX 332				-	File Wrapper for U.S. Patent Number 5, 710,906	
712000	F	-		<u>\</u>		402 (patent not asserted), 403 (irrelevant and self-serving
DTV 224		11111		7		hearsay re: prior art and unasserted claims), Hearsay (801)
PTX 335				C	bug status and activity report	Hearsay (801), 403
PTX 336						Hearsay (801), 403
				Ì	Viber Applications Training	403 (miscellaneous compilation of documents); Hearsay
PTX 33/					ent Application	(001).
PTX 330					WITHDRAWN	
			· .	(C)	U.S. Utility Patent Application Number 09/374816 filed as a continuation from the 307 application	403 (application abandoned and not asserted), Hearsay
						(801).

	Confidential Triton Host Bridge Microarchitecture Specification	7	4/17/09	14/1-	PTX 364
	WITHDRAWN for duplicity. See PTX 362				PTX 363
	BUNIT Microarchitecture Specification	7	4117/09	4)	PTX 362
	WITHDRAWN for duplicity can pty sen				PTX 361
	Triton Data Path (TDP) - Component Specification Final (450DOC005366 - 450DOC005480)	-	100(11/4		***
	INTEL Confidential - Triton System Controller (TSC) and				PTX 360
	WITHDRAWN for duplicity. See PTX 358				PTX 359
	PCI Bridge - Preliminary Component Specification Draft	\	4/17/09	4/	
iraisay (out), 403				-	25. X.Ld
Learney (2011) 102	roduct (Product sold by				PTX 357
Hearsay (801), 403	Viper-M Multimedia Chipset Preliminary Data n/82C557M/82C558M	The second secon			PTX 356
Hearsay (801), 403	32C567/82C568 Viper-M				71X 355
Hearsay (801), 403		-			PIX 354
Hearsay (801), 403	6 Chipset Development	The state of the s			PIX 353
Hearsay (801), 403	OPTi Document re ViperMAX Status Update				P1X 352
Hearsay (801), 403	OPTi White Paper re PCI Concurrency and Bandwidth, Revision 0.2				P1X 351
Hearsay (801), 403	Compaq				
Hearsay (801), 403	Email re Compaq List of Action Items				PTX 349
Hearsay (801), 403	Email re Compaq List of Action Items	•			P1 X 348
	Data Sheet for 82430FX PCIset cache/memory subsystem. 82437FX System Controller, and 82438FX Data Path	7	4/17/09	11/1:	> 04
	core-logic market; Intel is king of the land, most others are losing chip-set share	4	4/17/09	14/1	
	Microprocessor Report Article re Vendors fight for Pentium		•		PTX 346
	Electronic News Article re OPTi, ALi Taking on Pentium Chipsets	1	4/11/09	1/4	PTX 345
	U.S. Patent Number 6,405,291 B1 for Predictive Snooping of Cache Memory for Master Initiated Accesses	7	4/17/09	14	PTX 344
	U.S. Patent Number 5,710,906 for Predictive Snooping of Cache Memory for Master Initiated Access				T1X 343
	Patent License Agreement between OPTi and Intel				P1X 342
	D2-5 Waveform for VT82C505 with handwritten notations	, , , , , , , , , , , , , , , , , , ,			PTX 341
	VIA VT832C505 Test Results Set #1	,			PTX 340
OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITIED	OFFERED MARKED	NO. OFFE	
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PTX 392		PTX 391	PTX 390	PTX 389	71X 388		7 > 30	PTV 386	P1 X 385	PIX 384	PTX 383	PTX 382	PTX 381	PTX 380	PTX 379	PTX 378	F1X 3//	12010	978 XTG	PTX 3/4	7.6 A.LC	7 > 5 3	DTV 979	PTX 372	PTX 371	P1X3/0	PTX 369	P1X 368	PTX 367	P1 X 366	PTX 365	NO.
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United States Patent Number 6,405,291 B1 re Predictive Snooping of Cache Memory for Master-Initiated Accesses	Snooping of Cache Memory for Master-Initiated Accesses; Handwritten notes re two sets of patents	United States Patent Number 5 710 906 re Predictive	Onti Viner Block Disoram	ViperMax Status Update	e, SO#,	memory for master initiated accessories	Ö	WITHDRAWN	SB700 A-Link Bridge Specification	PCI Bridge Block Specification	PCI Bridge Block Specification, revision 1.4	SB600 A-Link Bridge Specification	Power Point presentation re PCIB Background	SB600 Scope of Work Document, Revision 0.39	WITHDRAWN	SB700 and SB600 Delta, version 0.12	AMD RS690/RS485 Series IGP Motherboard Design Guide	3.01	AMD Short Register Reference Manual, Revision 3.01	WITHDRAWN	designs	Collection of block diagrams - Piranha, Hamachi, Flathead, Grouper, Bowfin, Moray, Yokohama, and Herring reference	3.03	SB600 Data Book, Technical Reference Manual Revision	ATI Chipset Feature Matrix	Email from: Mark Williams to Richard Heye, RE: overview of patents, July 25, 2000	WITHDRAWN	WITHDRAWN	WITHDRAWN	WITHDRAWN	WITHDRAWN	DESCRIPTION OF EXHIBITS
		Hearsay (801), 403	nearsay (out), 403	Horrow (804) 409	invoice, units, Hearsay (801), 403		Hearsay (801), 403 (AMD case is irrelevant).		Hearsay (801), 402, 403, F	Hearsay (801), 402, 403, F	Hearsay (801), 402, 403. F	Hearsay (801), 402, 403, F	Hearsay (801) 402 403 F	Hearsay (801), 402, 403, F		Hearsay (801), 402, 403, F	Hearsay (801), 402, 403, F	Hearsay (801), 402, 403, F	Hearsay (801), 402, 403, F			Hearsay (801), 402, 403, F	i real easy (00 1), 402, 403, 1	Hearson (801), 102, 103, 1	Hearsay (801) 402 403 E	Hearsay (801), 402, 403, 1001, 1002, F						OBJECTIONS

PLF NO.	DEF	DATE	MARKED	ADMITTED	DESCRIPTION OF EXHIBITS	() X X X X X X X X X X X X X X X X X X
DTV 202	NO.	OFFERED				OBJECTIONS
-12.090	- Fresht ed				Request for Further Consideration of IDS Documents D2-1 Through D2-7 re Predictive Snooping of Cache Memory for Master-Initiated Accessor	
PTX 394					U.S. Utility Patent Application no. 09/631564 entitled	
		41.71 6			predictive snooping of cache memory for master-initiated accesses; Advance Information MP105 PCI Bridge/Memory	
PTX 395		11.10		7	Controller Hardware Specifications WITHDBANKI	
PTX 396					WITHDRAWN for duplicity. See PTX 395	
PTX 397					WITHDRAWN	
PTX 398		1-14		/	Unexamined Japanese Patent Application Publication No. H7-	
7		Latin	-	V		
PTX 399		No section of		,	WITHDRAWN for duplicity. See PTX 398	
PTX 400		,			Declaration of Subir Ghosh in Support of Request for Further	
		-			Consideration of IDS Documents D2-1 Through D2-7 re	
		111109		1	Cache Memory for Master-Initiated Accesses	
PTX 401		4117/09		7	PCI Local Bus Specification, revision 2.2	
PIX 402					PCI Local Bus Specification, revision 2.2	
F4X 403		4/17/09		1	PCI-X Addendum to the PCI Local Bus Specification- Revision 1.0	
PTX 404		411/09		1	PCI Special Interest Group, PCI-X Overview	
C04 X17		1/17/09			OPTi, Inc. Technology License Agreement between OPTi, Inc. Hearsay (801), 403 and OPTi Technologies, Inc., September 12, 2002	Hearsay (801), 403
PTX 406					United States Patent Number 5,630,094 re Integrated Bus	
		-			Bridge and Memory Controller That Enables Data Streaming	
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PTX 413		17)09			ed-Memory	Hearsay (801), 402, 403
PTX 414		4)17/09/			d Data Prefetching in Multiprocessors with	Hearsay (801), 402, 403
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PTX 461		100		7	File History of US Patent 5.710.906	
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PTX 530		•		\	eMac Developer Note	403 (technical document has limited probative value
		4/17/09		,		without supporting testimony and may confuse the jury if introduced wholesale)
PTX 531					Block diagram for eMac G4	403 (technical document has limited probative value
		4/17/09				without supporting testimony and may confuse the jury if introduced wholesale)
PTX 532		_			Book Developer Note	403 (technical document has limited probative value
		4/17/09		\		without supporting testimony and may confuse the jury if introduced wholesale)
PTX 533					Book Developer Note	403 (technical document has limited probative value
		14/11/100		_		without supporting testimony and may confuse the jury if
PTX 534		-			Block diagram for iBook G4	403 (technical document has limited probative value
		11/11/20				without supporting testimony and may confuse the jury if
PTX 535					Mac G5 Daveloner Note	introduced wholesale).
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		1117101		1		introduced wholesale)
PTX 537					iMac G5 Developer Note	403 (technical document has limited probative value
		1/1/09		1		without supporting testimony and may confuse the jury if introduced wholesale).
PTX 538					Mac mini Developer Note	403 (technical document has limited probative value
		17/17/189		1		without supporting testimony and may confuse the jury if introduced wholesale)
PTX 539					Block diagram for Mac Mini G4	403 (technical document has limited probative value
		4/17/1891		1		without supporting testimony and may confuse the jury if introduced wholesale).
PTX 540					12-inch PowerBook G4 Developer Note	403 (technical document has limited probative value
		4/17/09				without supporting testimony and may confuse the jury if introduced wholesale).
PIX 541		, /		\	Block diagram for PowerBook G4	403 (technical document has limited probative value
		4/17/09				without supporting testimony and may confuse the jury if introduced wholesale)
P1X 542				\	15-inch PowerBook G4 Developer Note	403 (technical document has limited probative value
		11/1/109				without supporting testimony and may confuse the jury if introduced wholesale).
- r I X 343		<u> </u>		\	17-Inch PowerBook G4 Developer Note	403 (technical document has limited probative value
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Hearsay (801), 403	2				P1X /05
Hearsay (801), 403	Summary of Damages Per I/O Port Royalty, Q2 FY 2005 - Q1 Hearsay (801), 403 FY 2009	<	0	4/17/09	F1X /04
Q2 FY 2005 Hearsay (801), 403	U. S. Revenue Per Unit, XSERVE G5, Apple, Inc. Q2 FY 2005	-	9	11/1/09	P1X /03
Hearsay (801), 403		7	99	117/09	T X 702
Hearsay (801), 403	U. S. Revenue Per Unit, IBOOK G4, Apple, Inc. Q2 FY 2005 - Hearsay (801), 403 Q1 FY 2009	7	9	4/17/09	PIX /01
Hearsay (801), 403	U. S. Revenue Per Unit, POWERMAC G5, Apple, Inc. Q2 FY 2005 - Q1 FY 2009	7	2	10/11/09	PIX /00
Hearsay (801), 403		<	2-	4)17/09	P1X 699
Hearsay (801), 403	5-	7	-0	10/11/09	P1X 698
Hearsay (801), 403		7	9	Palrill	F1X 69/
	WITHDRAWN				PTX 696
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OBJECTIONS	DESCRIPTION OF EXHIBITS	ADMITTED	MARKED	EF DATE D. OFFERED	PLF NO. DEF

	MacMini Buyer Survey			17 17 00		PIX /38
	PowerBook Recent Buyer Survey			7 1 00		FIX 737
	iBook Buyer Profile Study	1		1101		77.70
	Disk Controller (examined by Chris Brandin)			2011		257 YTG
	CPU, U3 (CPC925) Northbridge support chip, and K2 I/O Device and	\				
	Apple Power Mac G5 computer containing a Dougle DC 070FV					PTX 735
	International Symposium on Computer Architecture, Tokyo, June 1996	-		4/11/09		
	Support by the IEEE Futurebus", Procd. 13th Annual	\		-		
	A Class of Compatible Cache Coherency Protocols and Their					71 / 04
	Spreadsheet re Vendetta	K		401112		DTV 704
	Daytimer of Michael Collins	1				F 1 A / 3Z
		1				707
	AMD-8131 Hypertransport PCI-X Tunnel Data Sheet			F		T / 731
	declaration of George Richard Hayek	7				064 ALG
	Statement of Peter William Ward, Exhibit PWW 2Statutory	\		2/2/20		67 / 71
	Preliminary PCI System Design Guide, revision .06	K		10114		T 1 / 1 / 20
	TriFles/PCI Architecture White Paper			E		T \ 720
	Spreadsheet re information	7		E		P 1 X 126
	US Patent 5,359,715	<		117		P1X /25
Hearsay (801), 403	- Q1 FY 2009					
11	II S Revenue Per I Init YSERVE CE Apple Ino Co Typone			_ [PTX 724
Hearsay (801), 403	U. S. Revenue Per Unit, POWERBOOK G4, Apple, Inc. Q2 FY 2005 - Q1 FY 2009	_		41109		P1X /23
Q2 FY 2005 - Hearsay (801), 403	Q1 FY 2009	7		417/09		17 V 7702
Hearsay (801), 403		7		4/17/09		207 YTG
Hearsay (801), 403	- Q1 FY 2009	7		120/21/11		PTX 791
Hearsay (801), 403	U. S. Revenue Per Unit, EMAC G4, Apple, Inc. Q2 FY 2005 - Q1 FY 2009	5		17/09		81 / V1.1
Hearsay (801), 403	U. S. Revenue Per Unit, IMAC G5, Apple, Inc. Q2 FY 2005 - Q1 FY 2009	\		19/11/18		P1X / 18
	WITHDRAWN					P1X 747
Hearsay (801), 403	Total Firewire Ports Supported by OPTi Patents-in-Suit, Apple, Inc., Q2 FY 2005 - Q1 FY 2009	7		4/11/09		PIX /16
Hearsay (801), 403	Number of I/O Port Supported by OPTi Patents-in-Suit, Apple Computer Systems	7		417/8		P1X /15
Hearsay (801), 403	I/O Port Supported by OPTi Patents-in-Suit, XSERVE G5, Apple, Inc., Q2 FY 2005 - Q1 FY 2009	(m				PTX 714
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	macorph from Disk of Steve This	1		4/20/09	アファンチン
	Disk of Steve Jobs	7		4/20 09	17x746
	Applications		-	21.10	#
	Unlocks Pentium Processor Power Necessary for Multitude of	<u>_</u>	<u> </u>	2 1 X	
	Pentium Business Wire: Processors; Triton Architecture	•		-	
	Intel Corp. Introduced Higher Performing PCI Chip Set for		_	•	PTX 745
	Group ExhibitBrandin Testing Results	K	9	470	PTX 744
	CV of Alan Smith	7	4	41710	PTX 743
	CV of Chris Brandin	7		7117106	PTX 742
	Apple iMac G5 Buyer Survey Wave 1: U.S. Report	7	22	2012114	PTX 741
	CV of Roy Weinstein	7		111/100	PTX 740
	Apple 10-K	7		417109	PTX 739
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